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266/8-1502

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:  
Sandeep Khanna

Application No.: 09/815,778

Filed: March 24, 2001

Title: Method and Apparatus For Performing  
Priority Encoding In a Segmented  
Classification System Using Enable  
Signals

Attorney Docket No. NLM.P003

Examiner: Not Yet Assigned

Art Unit: 2661

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Assistant Commissioner for Patents  
Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits under 37 CFR §1.97(b). Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1624.

The references listed in the attached PTO Form PTO/SB/08A, copies of which are attached, may be material to examination of the above-identified patent application. Applicant submits these references in compliance with his duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.


**CERTIFICATE OF MAILING**

Date of Deposit August 8, 2002

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231 on the date indicated above.

Lawrence M. Cho

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This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If there are any additional charges, please charge them to Deposit Account No. 50-1624.

Respectfully submitted,

Dated: 8/8, 2002

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PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO		<b>Complete if Known</b>			
		Application Number	09/815,778		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Filing Date	March 24, 2001		
		First Named Inventor	Sandeep Khanna		
		Art Unit	2661		
		Examiner Name	Not Yet Assigned		
Sheet	1	of	7	Attorney Docket Number	NLM.P003

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. 1	Document Number Number - Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 3,257,646	06-21-1966	Roth	
		US- 3,353,159	11-14-1967	Lee, III	
		US- 3,602,899	08-31-1971	Lindquist et al.	
		US- 3,675,211	07-04-1972	Raviv	
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FOREIGN PATENT DOCUMENTS							
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		Country Code <sup>3</sup>	-Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
		EP	0 267 612 A2	05-18-1988	Akita		
		EP	0 381 249 A2	08-08-1990	Akashi		
		EP	0 660 332 A1	06-28-1995	Montoye		
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		WO	98/12651	03-26-1998	Kemoke		
		WO	99/31581	06-24-1999	Therkildsen		

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		Application Number	09/815,778		
		Filing Date	March 24, 2001		
		First Named Inventor	Sandeep Khanna		
		Art Unit	2661		
		Examiner Name	Not Yet Assigned		
Sheet	4	of	7	Attorney Docket Number	NLM.P003

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		Number - Kind Code <sup>2</sup> (if known)			
		US- 5,893,931	04-13-1999	Peng et al.	<b>RECEIVED</b>  <b>AUG 13 2002</b>  <b>Technology Center 2600</b>
		US- 5,920,886	07-06-1999	Feldmeier	
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		Application Number	09/815,778
Sheet	5	Filing Date	March 24, 2001
		First Named Inventor	Sandeep Khanna
		Group Art Unit	2661
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	NLM.P003

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
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		PANKAJ GUPTA and NICK MCKEOWN, "Packet Classification on Multiple Fields", SIGCOMM (September 2, 1999), 14 pages.	
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		HIROSHI KADOTA et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	

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		<b>Attorney Docket Number</b>	NLM.P003
Sheet	6	of	7

<b>OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS</b>			
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		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	
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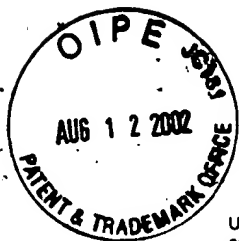
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		MUSIC Semiconductors, Advance Information, "MUAA Routing CoProcessor (RCP) Family", (October 1, 1998), Rev. 1b, pages 1-16.	
		TAKESHI OGURA et al., "A 4-kbit Associative Memory LSI", IEEE Journal of Solid-State Circuit, Vol. SC-20, No. 6, December 1985, pp. 1277-1281.	
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